

## WEST Search History





DATE: Saturday, February 25, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L58	(atm or packet\$1) and L57	63
<input type="checkbox"/>	L57	(add or added or adding or addition or adder) with (immediate adj (operand\$1 or value\$1))	229
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L56	(atm or packet\$1) and L55	94
<input type="checkbox"/>	L55	(add or added or adding or addition or adder) with (immediate adj (operand\$1 or value\$1))	354
		<i>DB=USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L54	US-5210835-A.did.	1
<input type="checkbox"/>	L53	US-4941085-A.did.	1
		<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L52	(add or added or adding or addition or adder) and (immediate adj (operand\$1 or value\$1))	29
<input type="checkbox"/>	L51	(add or added or adding) and (immediate adj (operand\$1 or value\$1))	21
<input type="checkbox"/>	L50	(add or added or adding) with (immediate adj (operand\$1 or value\$1))	7
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L49	packet\$1 and L48	1
<input type="checkbox"/>	L48	atm and L47	3
<input type="checkbox"/>	L47	(add or added or adding) with (immediate adj (operand\$1 or value\$1))	286
<input type="checkbox"/>	L46	L45 not L34	69
<input type="checkbox"/>	L45	L27 with (immediate or constant\$1)	310
<input type="checkbox"/>	L44	packet\$1 and L43	24
<input type="checkbox"/>	L43	start bit\$1 with operand\$1	33
<input type="checkbox"/>	L42	L30 with operand\$1	0
<input type="checkbox"/>	L41	L37 with operand\$1	4
<input type="checkbox"/>	L40	L38 and L39	87
<input type="checkbox"/>	L39	L35 same L27	11233
<input type="checkbox"/>	L38	L35 and L37	87
<input type="checkbox"/>	L37	marker bit\$1	813
<input type="checkbox"/>	L36	L30 and L35	5
<input type="checkbox"/>	L35	L27 not L34	11233

<input type="checkbox"/>	L34	packet with address\$3	29817
<input type="checkbox"/>	L33	L27 same L30	1
<input type="checkbox"/>	L32	L27 and L30	38
<input type="checkbox"/>	L31	L28 and L30	0
<input type="checkbox"/>	L30	bit marker\$1	239
<input type="checkbox"/>	L29	start bit\$1 and L28	1
<input type="checkbox"/>	L28	operand\$1 with L27	86
<input type="checkbox"/>	L27	packet with add\$5	38478
<input type="checkbox"/>	L26	baum.in. and apple.asn.	4
<input type="checkbox"/>	L25	baum.in. and apple.asn. and multi-guage.ti.	0
<input type="checkbox"/>	L24	6047304.pn.	1
<i>DB=USPT; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L23	bit field arithmetic	2
<input type="checkbox"/>	L22	120 and L19	1
<input type="checkbox"/>	L21	(add or added or adding or adder or adds) and L19	3
<input type="checkbox"/>	L20	(add or added or adding or adder or adds) same L19	0
<input type="checkbox"/>	L19	6047304.uref.	3
<input type="checkbox"/>	L18	(add or added or adding or adder or adds) same L10	3
<input type="checkbox"/>	L17	(add or added or adding or adder or adds) same L11	1
<input type="checkbox"/>	L16	L11 not L15	23
<input type="checkbox"/>	L15	L11 not L14	66
<input type="checkbox"/>	L14	packet\$1 and L11	23
<input type="checkbox"/>	L13	214 and L12	1
<input type="checkbox"/>	L12	packet\$1 same L11	1
<input type="checkbox"/>	L11	dedicated logic circuitry	89
<input type="checkbox"/>	L10	dedicated logic device\$1	22
<input type="checkbox"/>	L9	packet\$1 same L8	1
<input type="checkbox"/>	L8	L7	692
<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L7	dedicated with logic device\$1	1269
<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L6	(carry save add\$2) with instruction\$1	4
<input type="checkbox"/>	L5	L4	0
<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L4	(carry save add\$2) with instruction\$1	32
<input type="checkbox"/>	L3	carry save add\$2 instruction\$1	0
<input type="checkbox"/>	L2	carry save add instruction	0
<input type="checkbox"/>	L1	5838960.pn.	1

END OF SEARCH HISTORY